

Date: March 21, 2024

**Advertisement for Part Time PhD Seat
under 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II'**

Applications are invited for the part time PhD position under 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II' at Thapar Institute of Engineering and Technology, Patiala from Academic Year 2024-25.

The details for part time PhD position are given below:

No. of positions: 01

Essential Qualifications: M.E/M.Tech.in Electronics Engineering/ Electronics and Communication Engineering/VLSI Design or other relevant areas with at least minimum CGPA of 6.00 on a 10-point scale or 60% (55% in case of SC/ST/Differently-Abled candidates) marks in aggregate where marks are awarded in the qualifying degree.

Desirable Qualifications: The applicants having a valid GATE/NET score shall be preferred.

Financial Benefits: Part Time PhD candidate will receive one-time Rs. 3.00 Lakhs through DBT in his/her bank account, on successful completion of PhD under Visvesvaraya PhD Scheme of MeitY (GOI).

Other Relevant Details:

1. Only candidates having excellent academic record and a keen interest in research are highly encouraged to apply.
2. The applicant will be responsible for the authenticity of information, other documents and photograph submitted.
3. Applicants in employment are required to submit a “**No Objection Certificate**” from the employer at the time of interview.
4. Complete information with regard to GATE score such as: year of passing, discipline, score, marks, All India Rank and the number of candidates appeared should be mentioned in the CV. Incomplete applications will be rejected.
5. No TA/DA will be paid.

How to Apply:

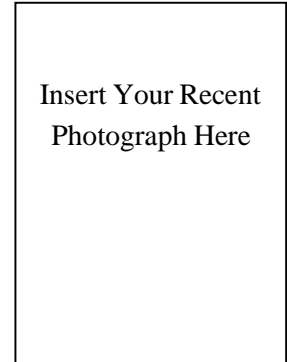
The candidates are requested to mail the soft copy of the filled-up application form(Annexure-I) to “**aheced1@thapar.edu**” with the subject of the email marked as “**Application for Part-Time PhD Position**” on or before **April 30, 2024**.

For more details kindly refer: <https://www.thapar.edu/admissions>

Annexure-I (Application Form)

**THAPAR INSTITUTE OF ENGINEERING & TECHNOLOGY, PATIALA
(Declared as Deemed-to-be University u/s 3 of UGC Act)**

- 1) Application for the post of:
- 2) Name of the Candidate in full (in capital letters):
- 3) Date of Birth:
- 4) Age :
- 5) Contact Information:
 - Postal Address:
 - Email:
 - Mobile Number:
- 6) Nationality:
- 7) Marital Status:
- 8) Category :
- 9) Educational Qualification



Sr. No	Name of Exam passed	Subjects/ Branch/ Specializations	Name of the University/ Board	Month & Year of exam passed	Percentage of Marks secured/ CGPA
1	Matriculation				
2	Higher Secondary				
3	B. E./B. Tech				
4	M.E./M.Tech				
5	Other				

- 10) Particulars of GATE examination:
 - 1) Year of passing:

2) Discipline:

3) Score (percentile):

4) All India Rank:

5) The number of candidates appeared:

11) Previous experience:

(Furnish details such as name of the post/fellowship, period worked, name of the organization/institution)

12) Present position:

(a) If you are studying, mention course/subject details:

(b) Details of fellowship/post:

(c) Name of the institution/ organization in which you are studying/working:

13) M.E./M.Tech. Thesis/Project title, with details of software language used:

14) Number of research papers published (only SCI):

15) Give contact details of two referees who are familiar with the candidate's work:

16) Any other relevant information which the candidate wishes to furnish:

Declaration

I declare that the above information is true and correct to the best of my knowledge and belief.

Date:

Place:

Signature of the applicant